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VERIES	190
VERYS	30
(L43 AND (NOP OR "NO" NEAR1 OPERATION) AND (VLIW\$1 OR VERY NEAR1 (LONG OR LARGE))) ).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	1

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<u>L44</u>	L43 and (nop or "no" near1 operation) and (vliw\$1 or very near1 (long or large))	1 <u>L44</u>
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<u>L42</u>	(may near2 philip or lucas near2 brian or moat near2 kent).in.	35 <u>L42</u>
<u>L41</u>	(mask\$5 and latch\$3 and datapath and (nop or "no" near1 operation) ).clm.	0 <u>L41</u>
<u>L40</u>	(mask\$5 and latch\$3 and datapath and (nop or "no" near1 operation) and (set or reset\$4) and compress\$5).clm.	0 <u>L40</u>
<u>L39</u>	(mask\$5 and latch\$3 and pipelin\$5 and (nop or "no" near1 operation) and (set or reset\$4) and compress\$5).clm.	1 <u>L39</u>
<u>L38</u>	(disabl\$4 and (nop or "no" near1 operation) and (bit\$1 or flag\$4 or tag\$4 or mask\$5) and identifier\$1 and instruction).clm.	2 <u>L38</u>
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<u>L37</u>	17 and 113	4 <u>L37</u>
<u>L36</u>	17 and 112	17 <u>L36</u>
<u>L35</u>	17 and 111	17 <u>L35</u>
<u>L34</u>	17 and 110	2 <u>L34</u>
<u>L33</u>	17 and 19	25 <u>L33</u>
<u>L32</u>	17 and 18	37 <u>L32</u>
<u>L31</u>	16 and 113	10 <u>L31</u>
<u>L30</u>	16 and 112	24 <u>L30</u>
<u>L29</u>	16 and 111	23 <u>L29</u>
<u>L28</u>	16 and 110	30 <u>L28</u>
<u>L27</u>	16 and 19	36 <u>L27</u>
<u>L26</u>	16 and 18	83 <u>L26</u>
<u>L25</u>	14 and 113	2 <u>L25</u>
<u>L24</u>	14 and 112	23 <u>L24</u>
<u>L23</u>	14 and 111	23 <u>L23</u>
<u>L22</u>	14 and 110	1 <u>L22</u>
<u>L21</u>	14 and 19	25 <u>L21</u>
<u>L20</u>	14 and 18	26 <u>L20</u>
<u>L19</u>	12 and 113	10 <u>L19</u>
<u>L18</u>	12 and 112	61 <u>L18</u>
<u>L17</u>	12 and 111	61 <u>L17</u>
<u>L16</u>	12 and 110	4 <u>L16</u>
<u>L15</u>	12 and 19	70 <u>L15</u>
<u>L14</u>	12 and 18	117 <u>L14</u>
<u>L13</u>	(717/136-163 ) [CCLS]	5169 <u>L13</u>
<u>L12</u>	(712/3,14,15,24,217,300) [CCLS]	1653 <u>L12</u>
<u>L11</u>	(712/3,14,15,24,300) [CCLS]	1093 <u>L11</u>

<u>L10</u>	(711/152-173)[CCLS]	15522	<u>L10</u>
<u>L9</u>	(712/2-28)[CCLS]	3310	<u>L9</u>
<u>L8</u>	(712/2-300)[CCLS]	13606	<u>L8</u>
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<u>L7</u>	L6 and (vliw\$1 or very near1 (long or large))	66	<u>L7</u>
<u>L6</u>	(disabl\$4 or enabl\$4) near45 nop\$1	414	<u>L6</u>
<u>L5</u>	(flag\$4 or tag\$4 or bit\$1 ) near25 mask\$7 near25 (nop or "no" near1 operation) and (vliw\$1 or very near1 (long or large) or superscalar)	33	<u>L5</u>
<u>L4</u>	mask\$7 near25 (nop or "no" near1 operation) and (vliw\$1 or very near1 (long or large))	70	<u>L4</u>
<u>L3</u>	L2 near45 (compar\$7 or mask\$5)	10	<u>L3</u>
<u>L2</u>	(nop or "no" near1 operation) near45 (vliw\$1 or very near1 (long or large))	221	<u>L2</u>
<u>L1</u>	("all" or "every" or "each") near15 (nop or "no" near1 operation) near45 (vliw\$1 or very near1 (long or large))	0	<u>L1</u>

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IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
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- ☐ 1. **The impact of Mpac 2**  
Purcell, S.;  
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- ☐ 2. **A retargetable VLIW compiler framework for DSPs with instruction-level parallelism**  
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[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)  
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- ☐ 3. **A modular VLIW Processor**  
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- ☐ 4. **A Scalable Network ASIP Enabling Flow Awareness in Ethernet Access**  
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- ☐ 5. **Heterogeneous multiprocessor for high definition video**  
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**6. Kernel formation in Garpcc**

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